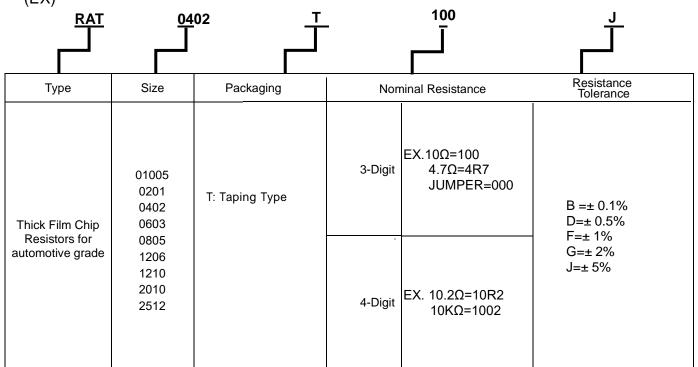


### 1 Scope:

- 1.1 This specification is applicable to lead free and halogen free of ROHS directive for RAT series thick film chip resistors.
- 1.2 This product is for automotive electronic application.
- 1.3 RAT01005/RAT0201 AEC-Q200 qualified, grade 1. Other Types AEC-Q200 qualified, grade 0.





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Written	Checked 上成伟	Approved	signing 仝幻霞	IT'S NOT UNDER CONTROL FOR PDF FILE PLS NOTE THE VERSION STATED	Issue Dep. <b>DATA Center.</b> Series No. <b>60</b>
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# **RAT Thick Film Chip Resistors** Automotive Grade Product Specification (Automotive Grade)

**Released Date** 

**Document No.** 

Page No.

2021/07/15

IE-SP-071

2

### **3 General Specifications:**

Rated Max. Max. Power Max. Max. Type at Working Overload			Max. Overload	T.C.R					JUMPER (0Ω) Rated Power		JUMPER (0Ω) Resistance Value	
,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	at 70℃	Voltage	Voltage	(ppm/℃)	B(±0.1%) E-24 \ E-96	D(±0.5%) E-24 ヽ E-96	F(±1%) E-24 ヽ E-96	G(±2%)、J(±5%) E-24	J (±5%)	F (±1%)	J (±5%)	F (±1%)
RAT	W	15V	30V	-200 +600			1Ω≦R	<10Ω	0.5A	0.5A	100mΩ	100mΩ
(01005)	32	137	300	±250			10Ω≦R≦	≦10MΩ	0.57	0.54	MAX	MAX
RAT	<u>1</u> _W	051/	501/	-200 +400		$1\Omega{\le}R{<}10\Omega$	$1\Omega\!\leq\!R\!<\!10\Omega$	$1\Omega\!\leq\!R\!<\!10\Omega$	0.54		50mΩ	
(0201)	20 W	25V	50V	±200	47Ω≦R≦ 1MΩ	$10\Omega{\leq}R{\leq}10M\Omega$	$10\Omega{\leq}R{\leq}10M\Omega$	$10\Omega{\leq}R{\leq}10M\Omega$	0.5A		MAX.	
RAT	<u>    1    </u> W	50V	100V	±100	$100\Omega{\leq}R{\leq}1M\Omega$	$10\Omega\!\leq\!R\!\leq\!1M\Omega$	$10\Omega{\leq}R{\leq}22M\Omega$	$10\Omega{\leq}R{\leq}22M\Omega$	1A	1.33A	50mΩ	35mΩ
(0402)	16	307	1001	±200			$1\Omega {\leq} R {<} 10\Omega$	$1\Omega \leq R < 10\Omega$		1.55A	MAX.	MAX.
RAT	<u>1</u> _W	75V	150V	±100	$100\Omega\!\leq\!R\!\leq\!1M\Omega$	$10\Omega\!\leq\!R\!\leq\!1M\Omega$	$10\Omega{\leq}R{\leq}22M\Omega$	$10\Omega{\leq}R{\leq}22M\Omega$	1A	2A	50mΩ	25mΩ
(0603)	10 750	1300	±200		$1\Omega{\leq}R{<}10\Omega$	$1\Omega\!\leq\!R\!<\!10\Omega$	$1\Omega\!\leq\!R\!<\!10\Omega$	IA	24	MAX.	MAX.	
RAT	<u>1</u> _W	1501/	2001/	±100	$100\Omega{\leq}R{\leq}1M\Omega$	$10\Omega{\leq}R{\leq}10M\Omega$	$10\Omega{\leq}R{\leq}27M\Omega$	$10\Omega{\le}R{\le}27M\Omega$	24	2.5A	50mΩ MAX.	20mΩ MAX.
(0805)	8	150V	300V	±200		$1\Omega{\leq}R{<}10\Omega$	$1\Omega\!\leq\!R\!<\!10\Omega$	$1\Omega\!\leq\!R\!<\!10\Omega$	2A			
RAT	W	200V	400V	±100	10Ω≦R≦ 1MΩ	$10\Omega{\leq}R{\leq}10M\Omega$	$10\Omega{\leq}R{\leq}27M\Omega$	$10\Omega{\leq}R{\leq}27M\Omega$	2A	3.5A	50mΩ	20mΩ MAX.
(1206)	4	2001		±200	$3\Omega\!\leq\!R\!<\!10\Omega$	$1\Omega{\leq}R{<}10\Omega$	$1\Omega\!\leq\!R\!<\!10\Omega$	$1\Omega\!\leq\!R\!<\!10\Omega$		0.0.1	MAX.	
RAT	<u>1</u> W	200V	400V	±100	$100\Omega{\leq}R{\leq}1M\Omega$	$10\Omega{\leq}R{\leq}10M\Omega$	$10\Omega{\leq}R{\leq}27M\Omega$	$10\Omega{\leq}R{\leq}27M\Omega$	2A	4A	50mΩ	20mΩ
(1210)	2	2007	400 v	±200			$1\Omega\!\leq\!R\!<\!10\Omega$	$1\Omega\!\leq\!R\!<\!10\Omega$	24	4A	MAX.	MAX.
RAT	<u>3</u> W	200V	400V	±100	$100\Omega{\leq}R{\leq}1M\Omega$	$10\Omega{\leq}R{\leq}10M\Omega$	$10\Omega{\leq}R{\leq}20M\Omega$	$10\Omega{\leq}R{\leq}20M\Omega$	2A	5A	50mΩ	20mΩ
(2010)	4	2007	400 V	±200			$1\Omega\!\leq\!R\!<\!10\Omega$	$1\Omega\!\leq\!R\!<\!10\Omega$	ZA	AC	MAX.	MAX.
RAT	1\\/	2001/	400\/	±100	$100\Omega{\leq}R{\leq}1M\Omega$	$10\Omega{\leq}R{\leq}10M\Omega$	$10\Omega{\leq}R{\leq}20M\Omega$	$10\Omega{\leq}R{\leq}20M\Omega$	2A	7A	50mΩ	20mΩ
(2512)	1W 200V 400V	2007 40	4007	±200			$1\Omega\!\leq\!R\!<\!10\Omega$	$1\Omega \leq R < 10\Omega$	ZA	78	MAX.	MAX.
Ор	erating Tem	perature Ra	ange			-55℃ ~ +15	5°C (01005/0201	1:-55℃ ~ +125℃	C)			

#### 3.1 Power Derating Curve:

3.1 Power Derating Curve:						
Туре	RAT(01005) / RAT (0201)	Other				
Operating Temperatu Range		−55°C ~ +155°C				
Explain	For resistors operated in ambient temperatures above 70℃, power rating shall be derated in accordance with figure below.	For resistors operated in ambient temperatures above 70℃, power rating shall be derated in accordance with figure below.				
Figure	70 100 80 60 40 20 40 20 40 60 40 20 40 60 40 40 20 40 40 40 40 40 40 40 40 40 4	70 100 0 0 0 0 0 0 0 0 0 0 0 0				
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### 3.2 Voltage Rating:

Rated Voltage: The resistor shall have a DC continuous working voltage or a rms. AC continuous working voltage at commercial-line frequency and wave form corresponding to the power rating, as determined from the following:

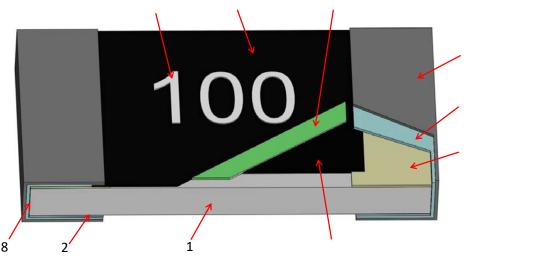
$$E = \sqrt{R \times P}$$

E= Rated voltage (v) P= Power rating (w) R= Nominal resistance( $\Omega$ )

### **4** Dimensions:

							Unit:mm
	Tune	Dimension	L	W	н	L1	L2
2873 ≥	Type RAT	Size Code01005	0.40±0.02	0.20±0.02	0.13±0.02	0.10±0.03	0 10+0 03
	RAT	0201	0.60±0.03	0.30±0.03	0.23±0.03	0.10±0.05	0.15±0.05
	RAT	0402	1.00±0.10	0.50±0.05	0.30±0.05	0.20±0.10	0.25±0.10
	RAT	0603	1.60±0.10	0.80±0.10	0.45±0.10	0.30±0.15	0.30±0.15
I <b>1</b> I 1	RAT	0805	2.00±0.10	1.25±0.10	0.50±0.10	0.35±0.20	0.35±0.15
	RAT	1206	3.05±0.10	1.55±0.10	0.50±0.10	0.45±0.20	0.35±0.15
<b>I</b>	RAT	1210	3.05±0.10	2.55±0.10	0.55±0.10	0.50±0.20	0.50±0.20
L2 L2	RAT	2010	5.00±0.20	2.50±0.20	0.55±0.10	0.60±0.20	0.60±0.20
	RAT	2512	6.30±0.20	3.20±0.20	0.55±0.10	0.60±0.20	0.60±0.20

### 5 Structure Graph:



1	Ceramic substrate	6	2nd Protective coating
2	Bottom inner electrode	7	Marking
3	Top inner electrode	8	Terminal inner electrode
4	Resistive layer	9	Ni plating
5	1st Protective coating	10	Sn plating

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### RAT Thick Film Chip Resistors Automotive Grade Product Specification (Automotive Grade)

Document No. Released Date

Page No.

IE-SP-071 2021/07/15 4

### 6 Reliability Test:

			Specifications			
	Item	Conditions		sistors	Jumper	
Tem Ex		erature in room temperature for 24±4hr or more, and measure of its resistance variance rate.		2.0% 1%:△R=±1.0% 5%:△R=±2.0%	Refer to item 3. general specifications	
	perature	Put the specimens in the High & low temperature test chamber with temperature varies from -55°C to 125°C for 15 minutes and total 1000 cycles. Then take them out to stabilize in room temperature for 24±4hr or more, and measure of its resistance variance rate.	△R=±2.0%		Refer to item 3. general specification	
	ort Time	Applied 2.5 times rated voltage for 5 seconds and release the load for about 30 minutes, then measure its resistance variance ate. (Rated voltage refer to item 3. general specifications)		2.0% 1%:△R=±1.0% 5%:△R=±2.0%	Refer to item 3. general specification	
Biased	d Humidity	the constant temperature humidity chamber with $85\pm2^{\circ}C$ and $85\pm5^{\circ}RH$ . Then apply the test voltage that calculates based on the 10% of rated power for 1000hrs. Then take them out to stabilize in room temperature for $24\pm4hr$ or more, and measure of its resistance variance rate.		5.0% 1% : △R=±2.0% 5% : △R=±3.0%	Refer to item 3. general specification	
	erational Life	the chamber with temperature of $125\pm3^{\circ}$ C and load the voltage for 1000 hours. Then take them out to stabilize in room temperature for $24\pm4hr$ or more, and measure of its resistance variance rate. Note: The input voltage shall refer to the power de-rating curve (referring to page 2,No.3.1)	$0.1\% \cdot 0.5\%$ ·	5.0% 1% : △R=±2.0% 5% : △R=±3.0%	Refer to item 3. general specification	
		Experiment evidence: AEC-Q200 The specimens are fully immersed into the Pb-free solder pot, then take them out to stabilize for 1 hour or more and measure of its resistance variance rate. Temp of solder pot : $260\pm5^{\circ}$ C Soldering duration : $10\pm1$ sec. Experiment evidence AEC-Q200			Refer to item 3. general specification.	
<u>.</u>					]	
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Document No. IE-SP-071

Released Date 2021/07/15

5

Page No.

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	Item		Cond	litions		Ros	Specifications sistors	s Jumper
	ESD	(2)dischar one (1) wi negative p 30min or r rate. The discharge	th a positive polari polarity. Afterwards more and measure test is performed w	I be applied to each ty and one (1) with a s, the specimens stal of its resistance var vith direct contact and or and capacitor used	bilize for iance d regular	∆R=±3.0%	55015	Refer to item 3. general specification
Sol	derability	Test meth Test item Preconditi The speci 4hrs±15m The speci immersed 235±5℃ observe th Test item The speci immersed 260±5℃ observe th	1 (solder pot test ion: mens are subjecte in. mens are immerse into the solder pot for 5+0/-0.5 sec. T ne soldering covera 2 (Leaching test) mens are immerse into the solder pot for 30+0/-0.5 sec.	t): Method B d to 155°C dry bake d into the flux first, th at a temperature of hen rinse with water age under the micros <b>Method D</b> d into the flux first, th at a temperature of Then rinse with water age under the micros	nen fully f and scope. nen fully f r and	2.At the edge of	verage over 95% of terminal, the ob eramic) shall not	oject underneath
	lectrical acterization	TCR(ppr R1: Resis R2: Resis T1: Room T2: Tempo	$\frac{(R2-I)}{(R1 (T2-I))} = \frac{(R2-I)}{(R1 (T2-I))}$ tance at room temp tance at -55°C or +7 temperature (°C) erature -55°C or +7	<del>₹1)</del> > 71) ×10 <sup>6</sup> perature (Ω) 125℃(Ω)		Refer to item 3 specifications	3. general	NA
-	ard Flex ding Test)	Solder the onto the B PCB, and 60 (+ 5) S load. Bending d (D) : 0402,0630 01005,020 2010 25	Bending Tester. Add the duration of the ec. Measure of its lepth 03 0805=5mm 01,12061210=3mm 12=2mm	test PCB and put th d force at the central applied forces shall resistance variance	part of be		l damage, peel-o	Refer to item 3. general <u>specification</u> ff of side end or
		⊫xperimer	nt evidence: AEC-0	1200		<u> </u>		
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**Document No. Released Date** 2021/07/15

Page No.

6

IE-SP-071

#### 7 Measurement Point:

Bottom electrode			Unit : mm
A	DIM	Α	В
	RAT0201	0.44±0.05	0.22±0.05
	RAT0402	0.80±0.05	0.24±0.05
	RAT0603	1.35±0.05	0.35±0.05
	RAT0805	1.80±0.05	0.35±0.05
	RAT1206	2.90±0.05	0.35±0.05
• Current Terminal	RAT1210	2.90±0.05	0.35±0.05
	RAT2010	4.50±0.05	1.15±0.05
Voltage Terminal	RAT2512	5.90±0.05	1.60±0.05

#### 8 Plating Thickness:

**8.1 Ni**:≧2 μ m

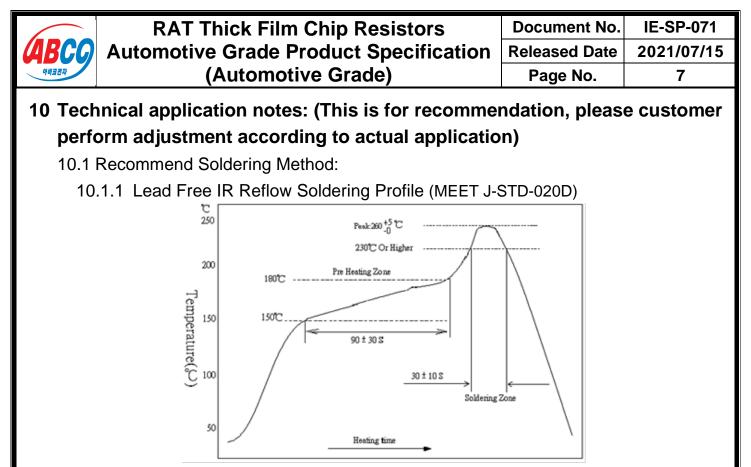
**8.2 Sn(Tin)**:≧3 μ m

8.3 Sn(Tin):Matte Sn

### 9 Rule of package empty quantity:

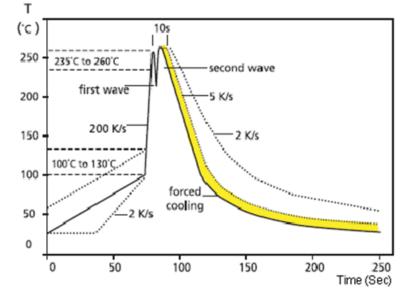
9.1 Each reel that empty quantities don't exceed 0.1% of whole quantities and continuous 2pcs (included) are allowed.

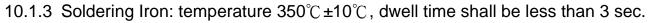
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10.1.2 Lead Free Double-Wave Soldering Profile.(This applies to 0603 size inclusive above products )



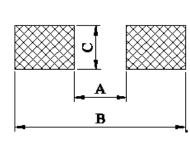


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10.2 Recommend Land Pattern Design (For Reflow Soldering) :

When a component is soldered, the resistance after soldering changes slightly depending on the size of the soldering area and the amount of soldering. When designing a circuit, it is necessary to consider the effect of a decrease or increase in its resistance.



			Unit:mm
	А	В	С
RAT01005	0.2	0.5	0.2
RAT0201	0.3	1.0	0.4
RAT0402	0.5	1.5	0.6
RAT0603	0.8	2.1	0.9
RAT0805	1.2	3.0	1.3
RAT1206	2.2	4.2	1.6
RAT1210	2.2	4.2	2.8
RAT2010	3.5	6.1	2.8
RAT2512	3.8	8.0	3.5

10.3 Automobile Electronic Application:

This specification is for automobile electronic use. RALEC will take no responsibility if any damage, cost or loss occurs when the product has been used in any special circumstances.

- (a) Information , entertainment , navigation , audio control units.
- (b) Comfortable door, window, seat control unit.
- (c) Internal lighting control unit.

### 10.4 Environment Precautions:

If consumer intends to use our company product in special environment or condition (including but not limited to those mentioned below), then will need to make individual recognition of product features and reliability accordingly.

- (a) Used in high temperature and humidity environment
- (b) Exposed to sea breeze or other corrosive gas, such as Cl2 H2S NH3 SO2 and NO2.
- (c) Used in non-verified liquids including water, oil, chemical and organic solvents.
- (d) Using non-verified resin or other coating material to seal or coat our Company product.
- (e) After soldering, it is necessary to use water-soluble detergents to clean residual solder fluxes, even though no-clean fluxes are recommended.

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10.5 Momentary Overload Precautions:

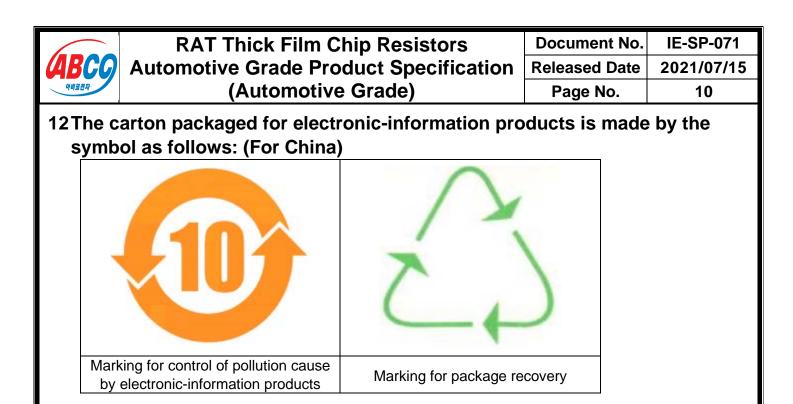
The product might be out of function when momentary overloaded. Please make sure to avoid momentary overloading while using and preserving.

- 10.6 Operation and Processing Precautions:
  - (a) Avoid damage to the edge of resistor and protective layer caused by mechanical stress.
  - (b) Handle with care when printing circuit board (PCB) is divided or fixed on support body, because bending of printing circuit board (PCB) mounting will make mechanical stress for resistors.
  - (c) Make sure the power rating is under the limit when using the resistor. When power rating is over the limit, the resister will be overloaded. There might be machinery damage due to the climbing temperature
  - (d) If the resister will be exposed under massive impact load (shock wave) in a short period of time, the working environment must be set up well before use.
  - (e) Please make evaluation and confirmation when the product is well used in your company and have a through consideration of its fail-safe design to ensure the system safety.

### 11 Stock period:

- 11.1 The temperature condition must be controlled at 25±5℃, the R.H. must be controlled at 60±15%. The stock can maintain quality level in two years.
- 11.2 Please avoid the mentioned harsh environment below when storing to ensure product performance and its' weldability. Places exposed to sea breeze or other corrosive gas, such as Cl<sub>2</sub> \ H<sub>2</sub>S \ NH<sub>3</sub> \ SO<sub>2</sub> and NO<sub>2</sub>.
- 11.3 When the product is moved and stored, please ensure the correct orientation of the box. Do not drop or squeeze the box. Otherwise, the electrode or the body of the product may be damaged.

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#### **13 Attachments:**

13.1 Document Revise Record(QA-QR-027)

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